

FIG. 1A

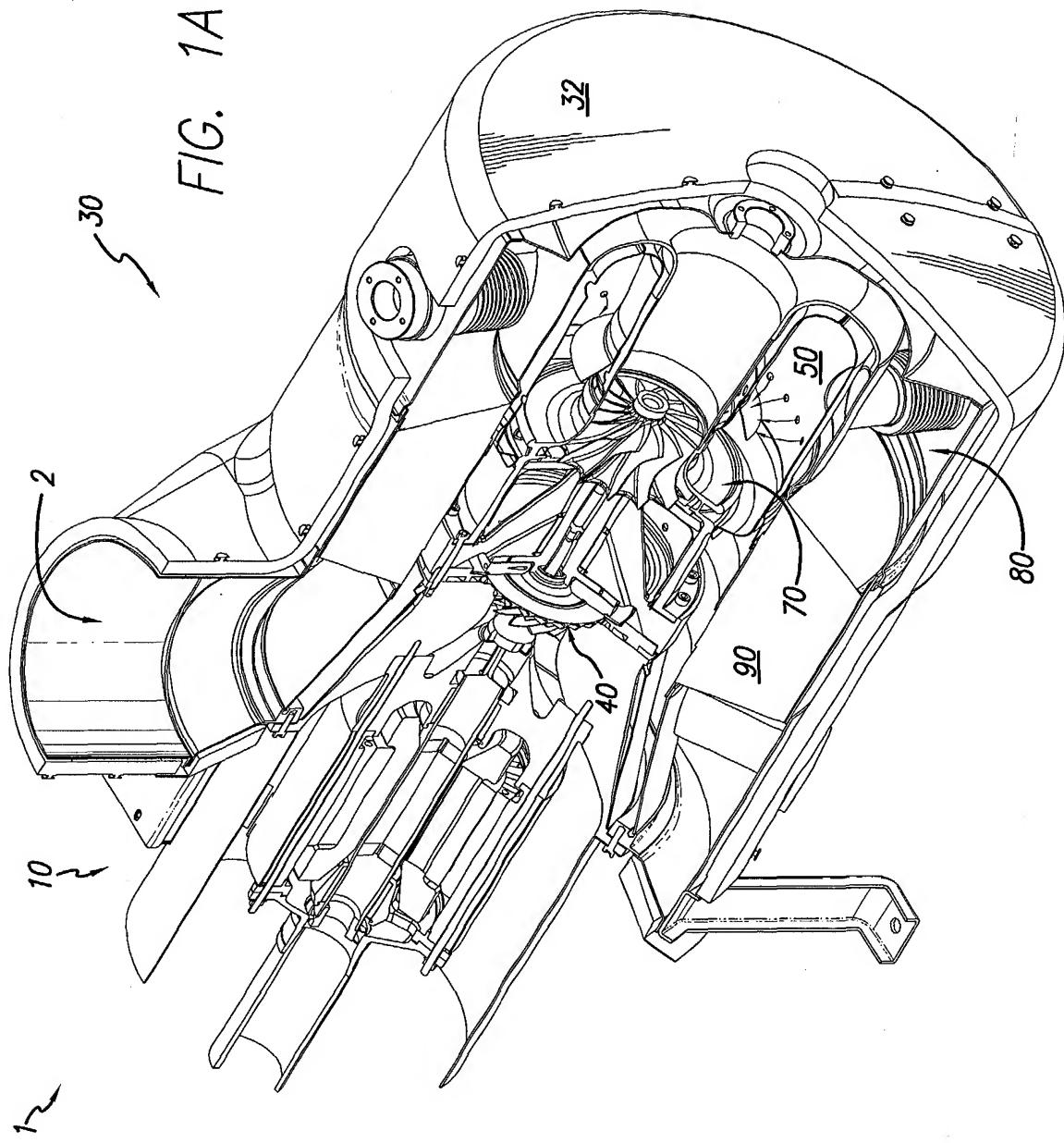
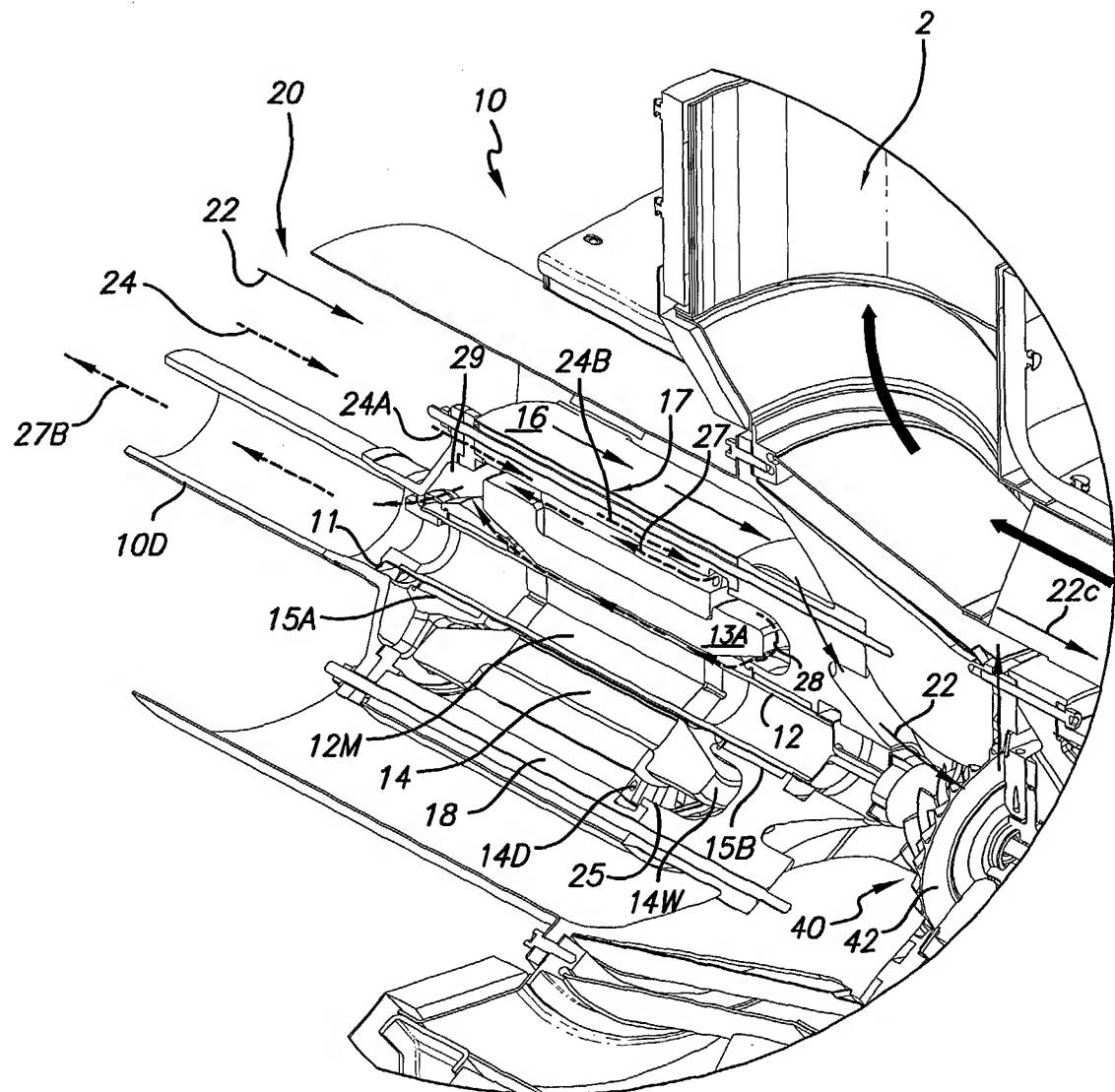


FIG. 1B



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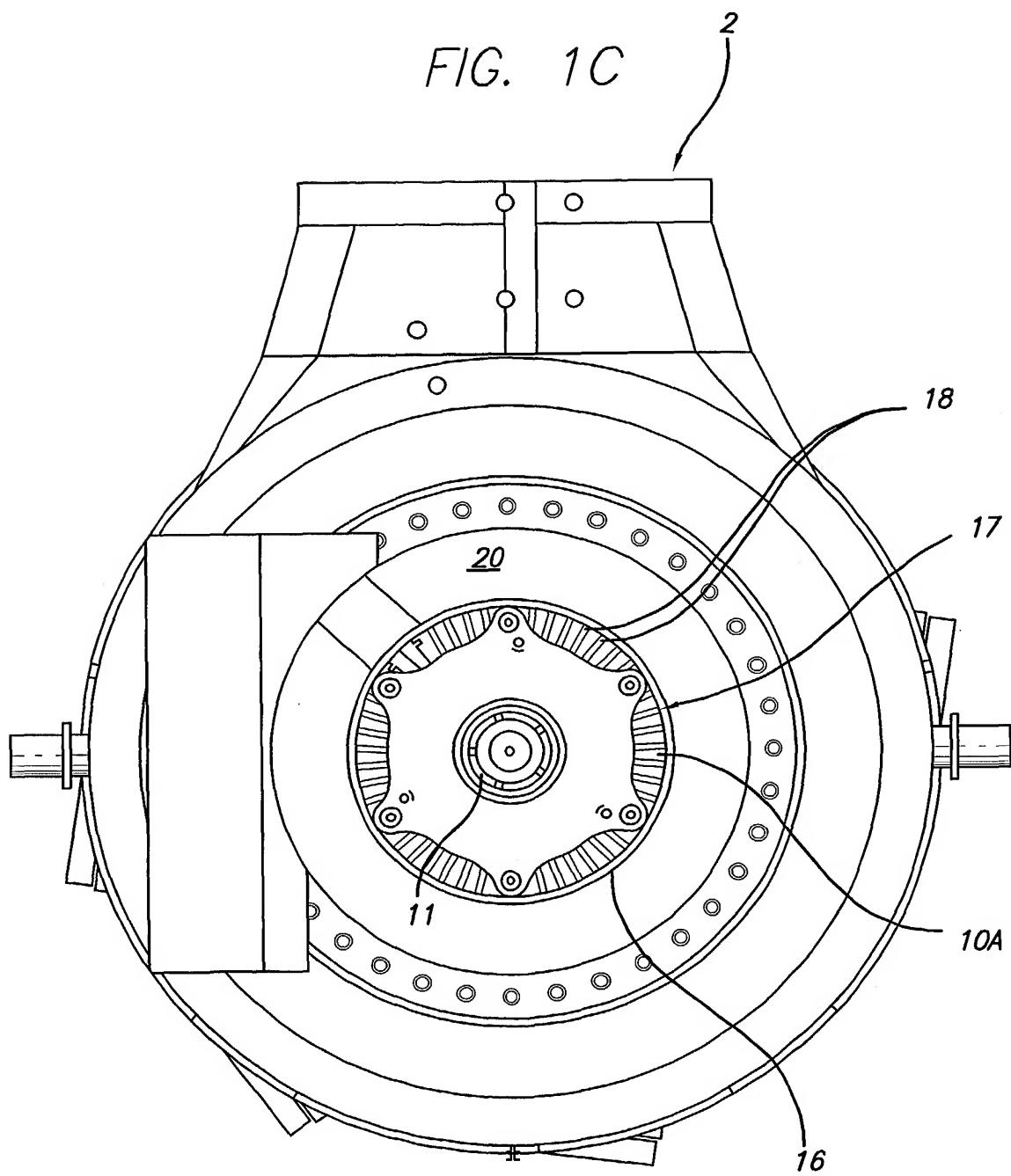
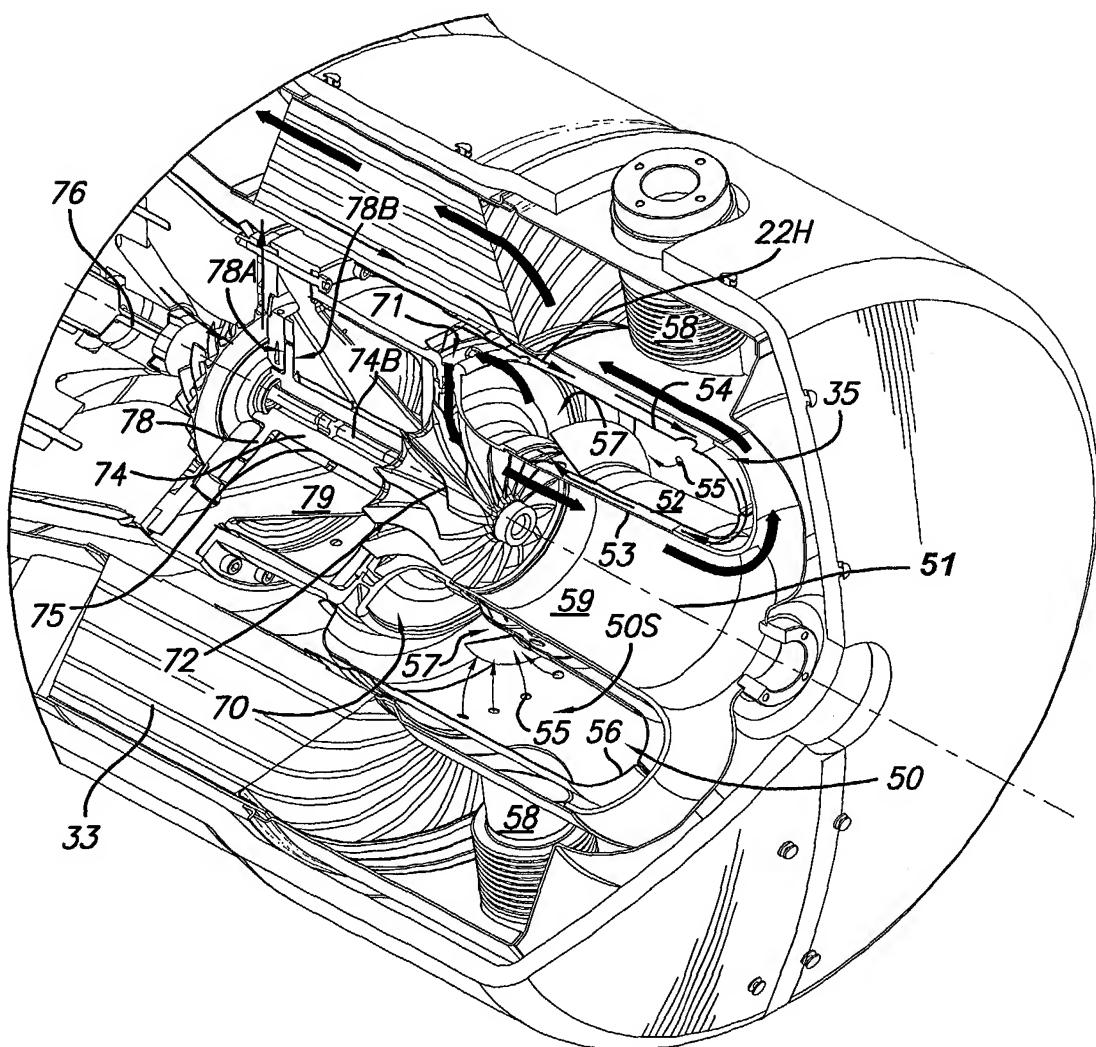


FIG. 1D



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FIG. 1E

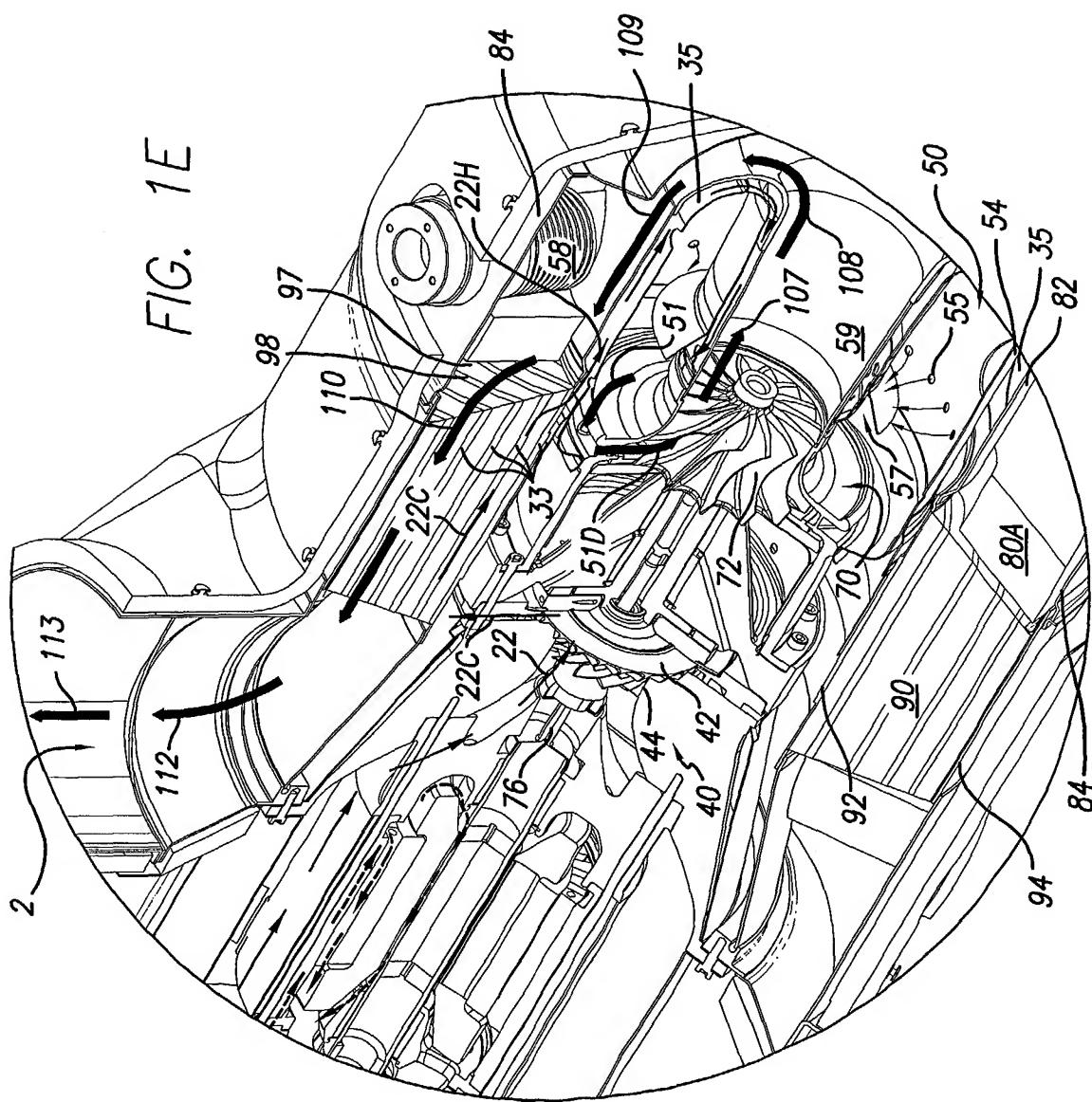
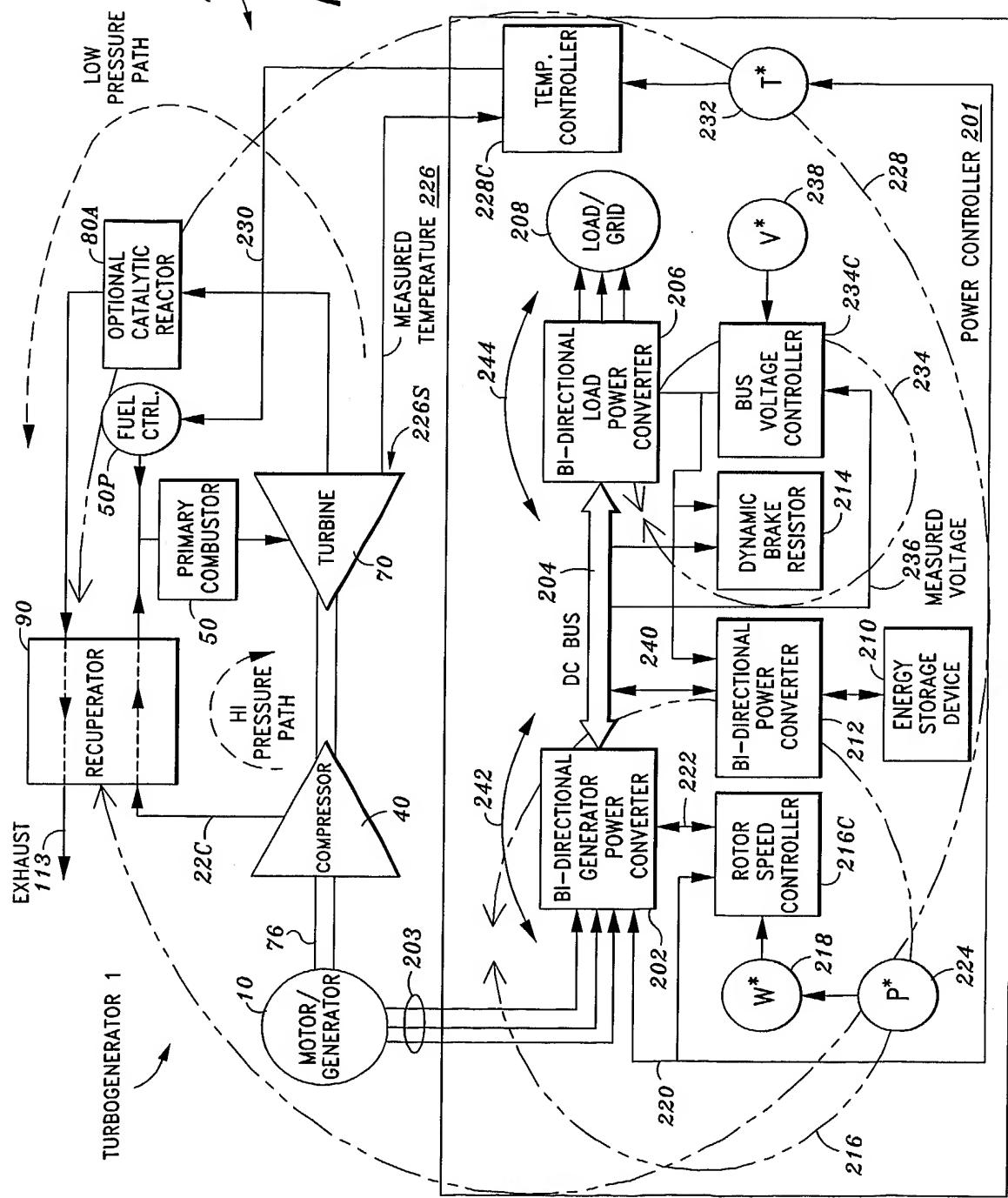


FIG. 2



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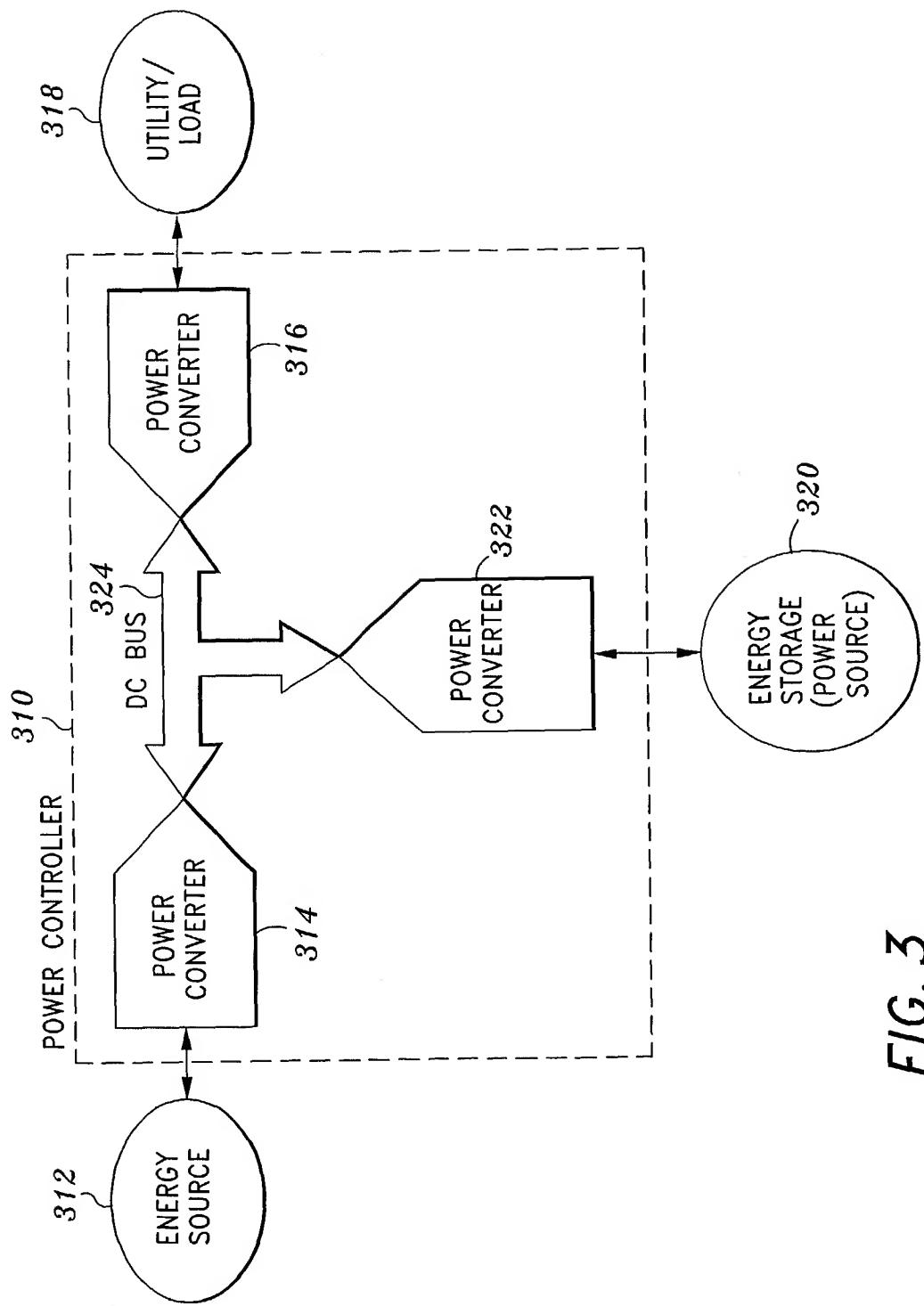


FIG. 3

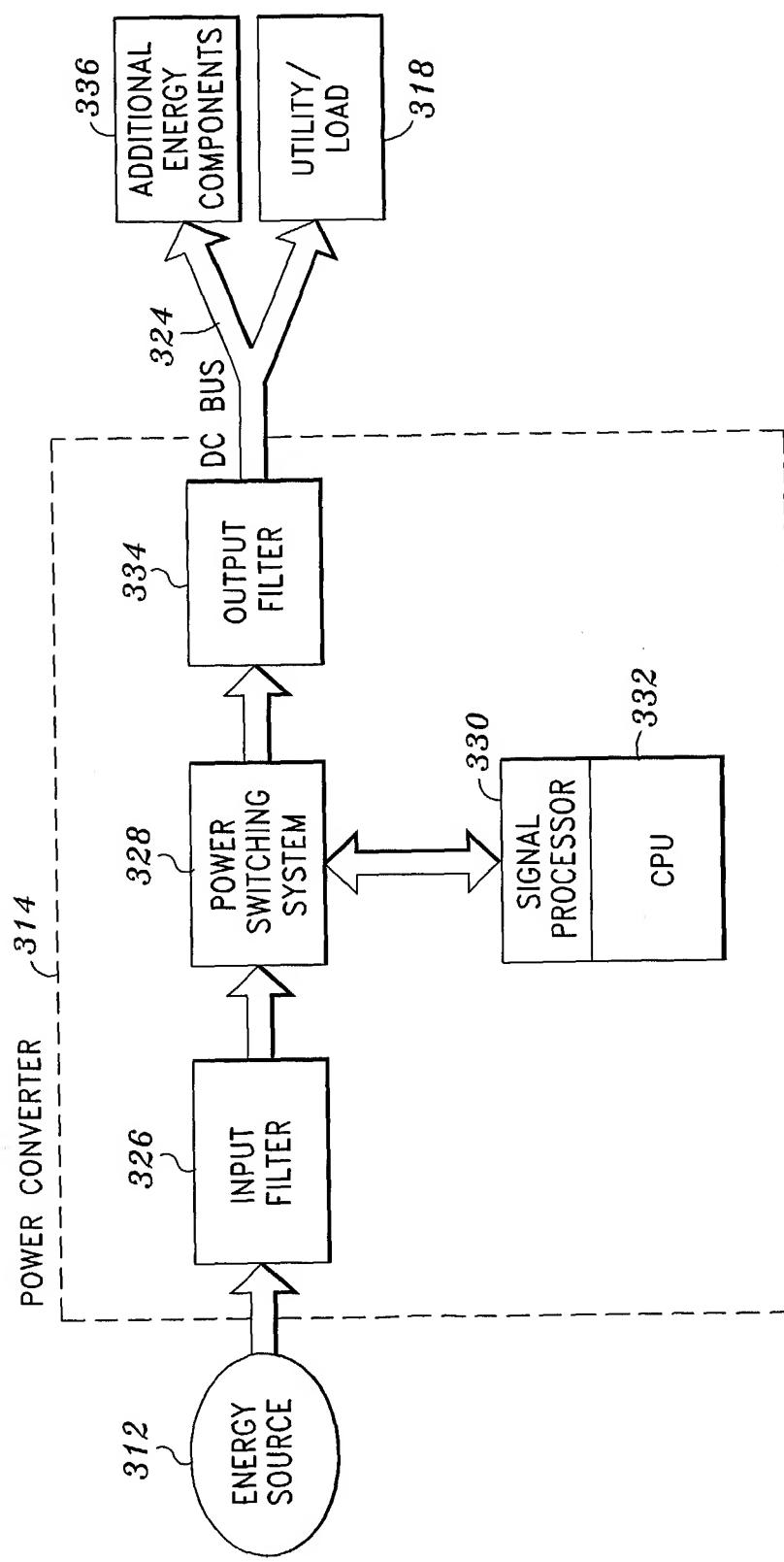


FIG. 4

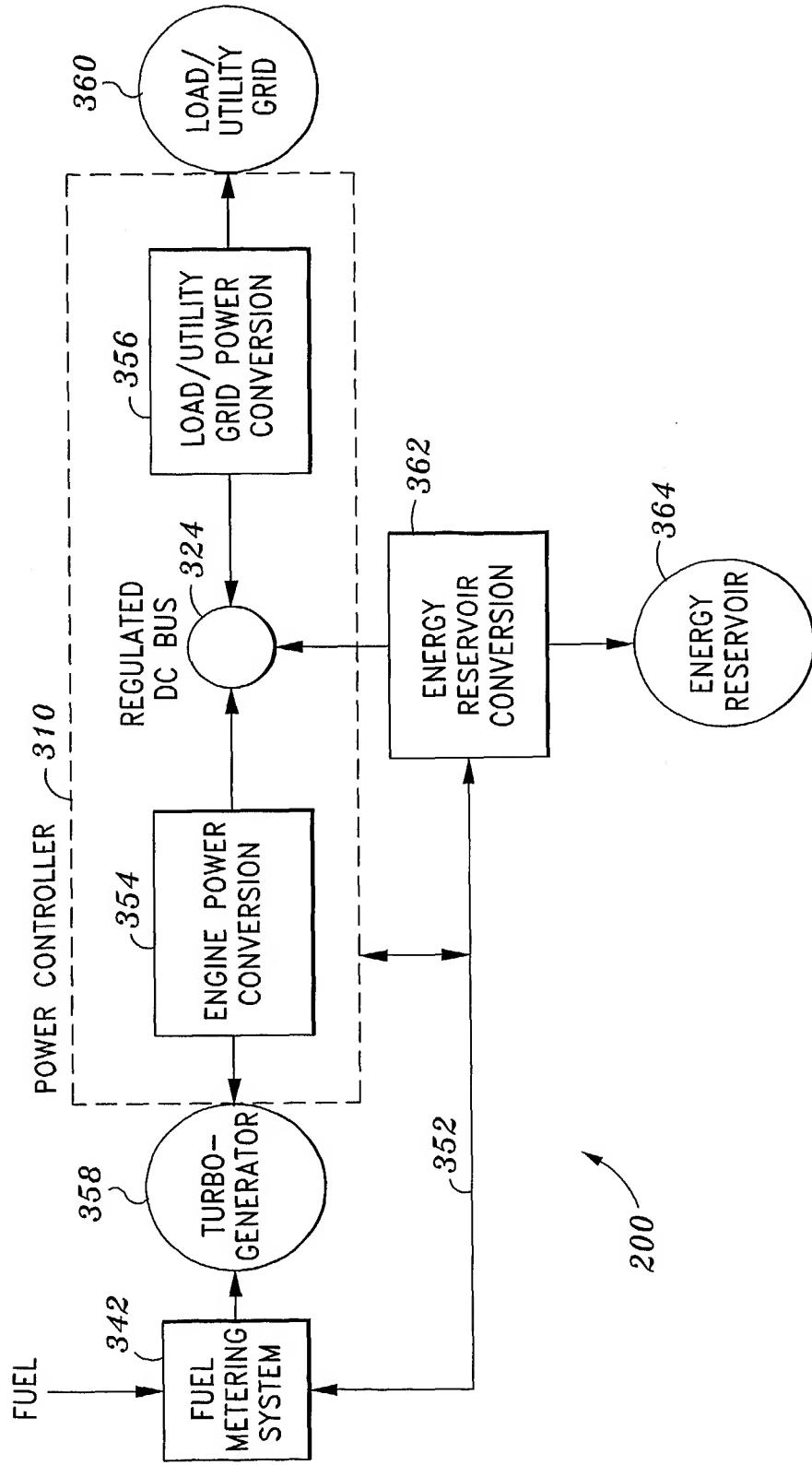
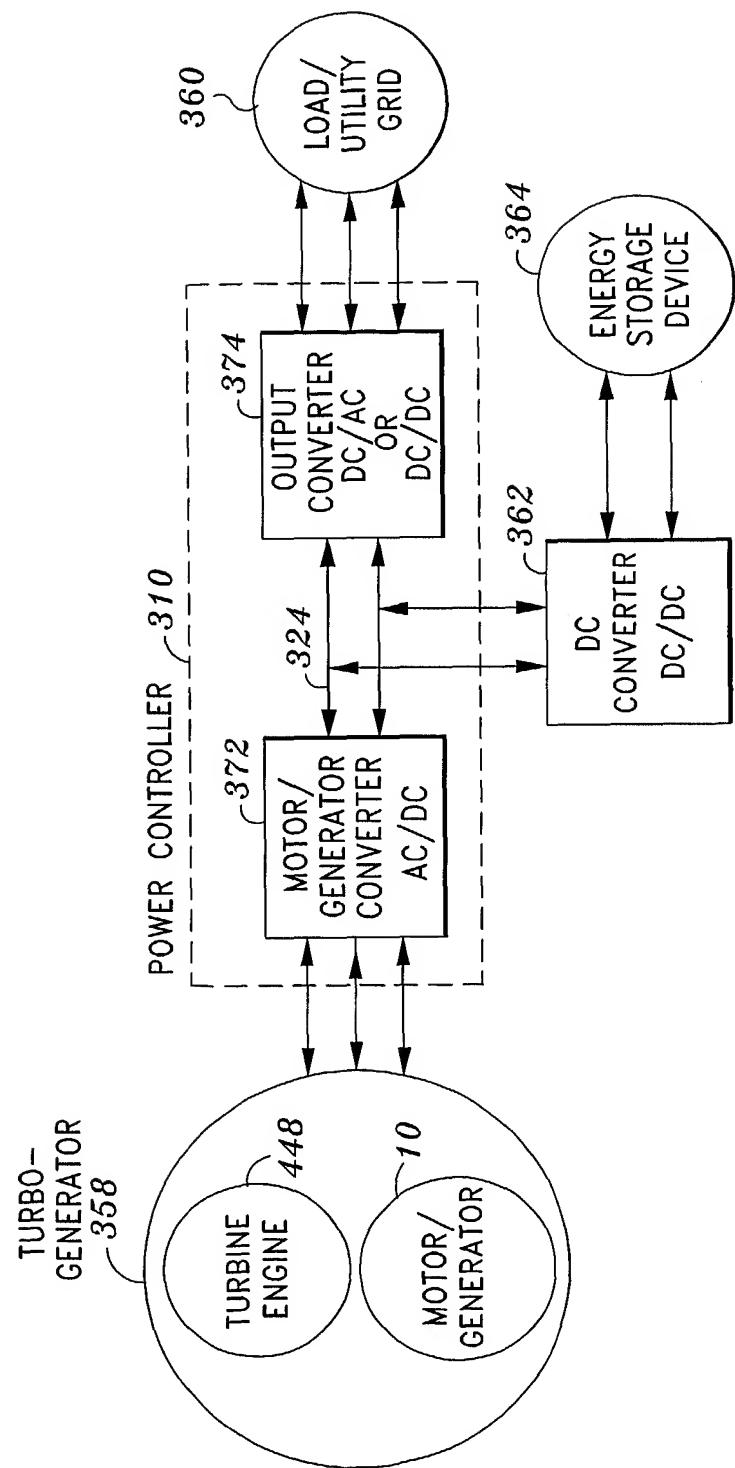


FIG. 5

FIG. 6



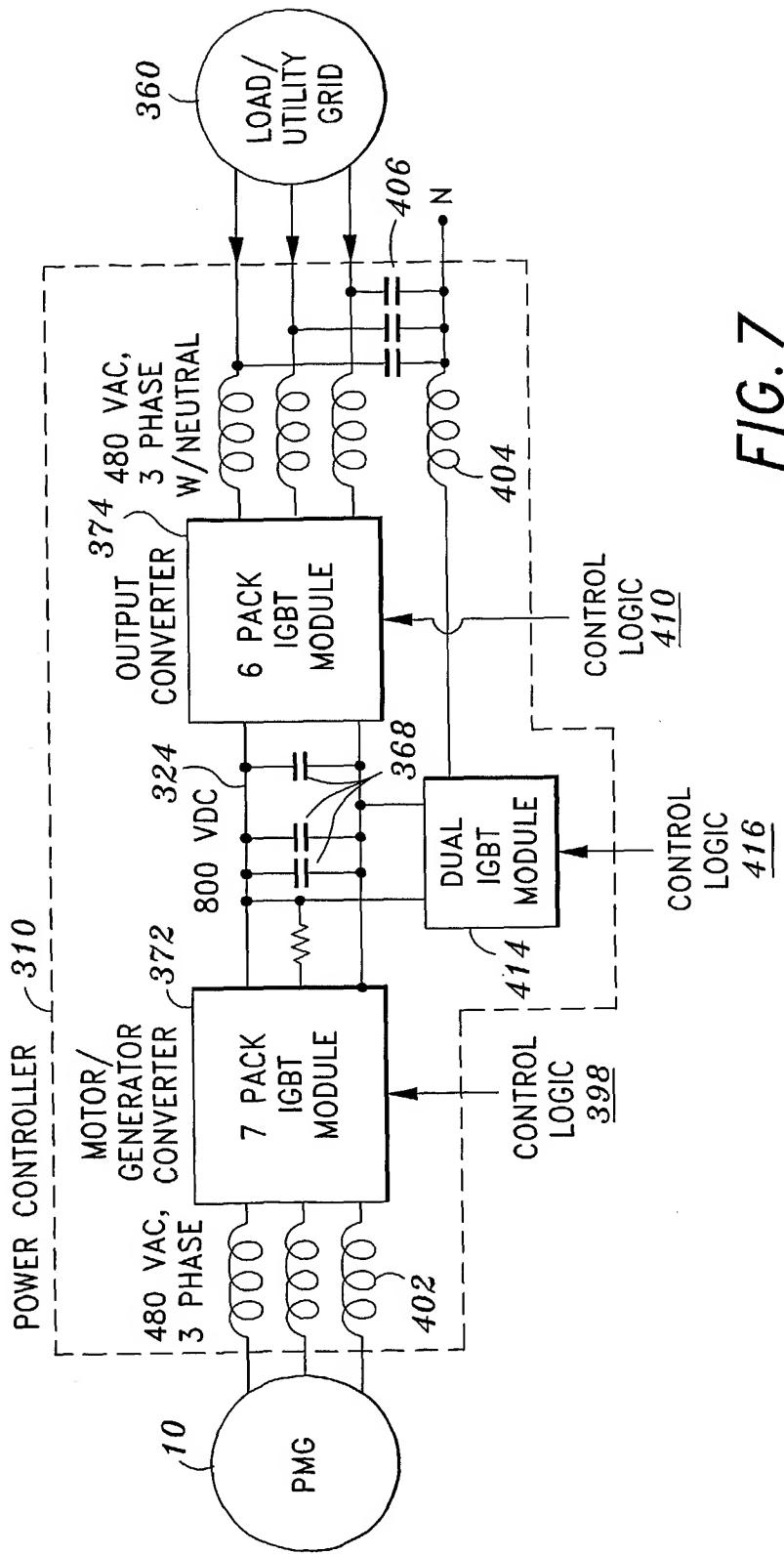


FIG. 7

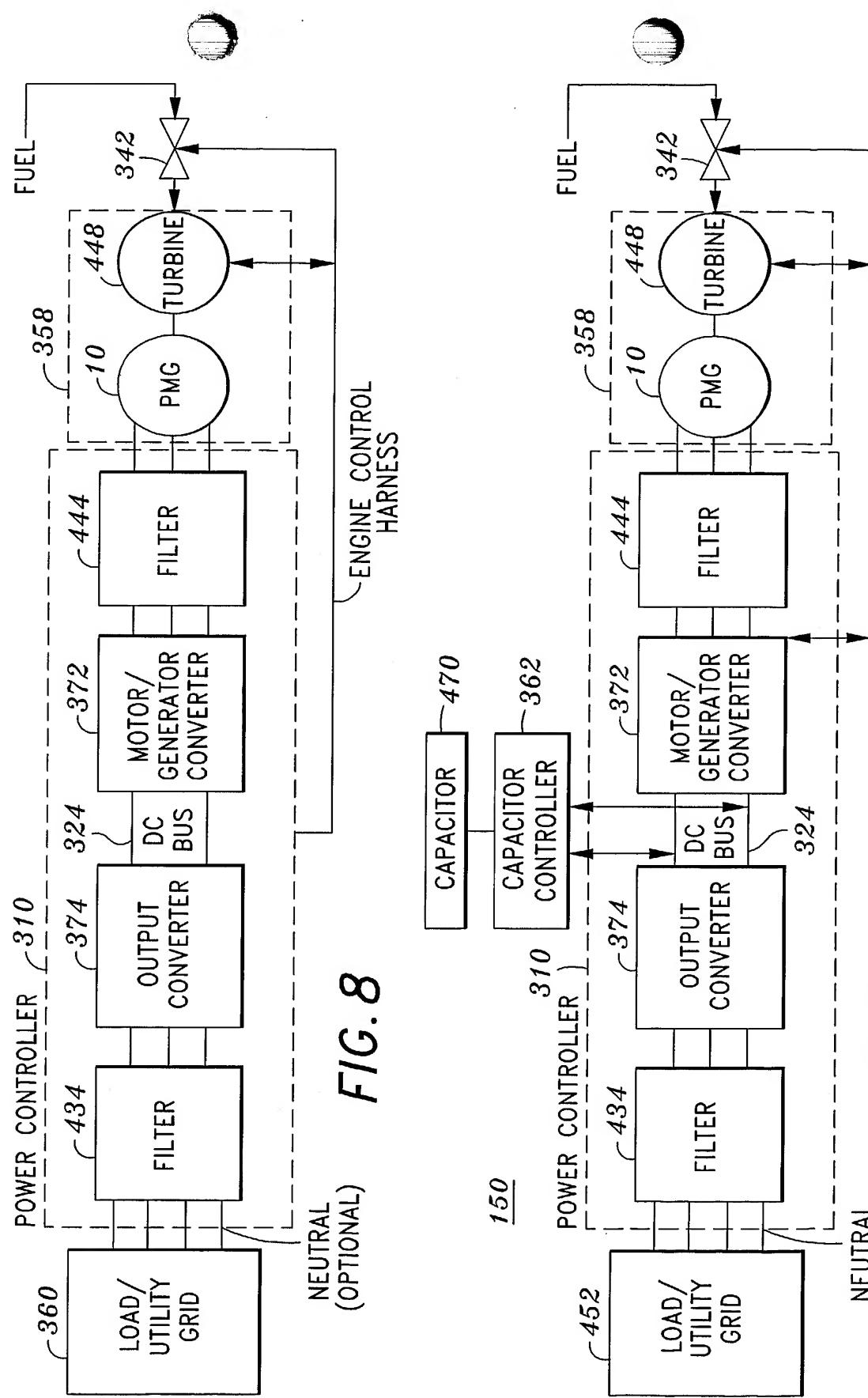


FIG. 8

FIG. 9

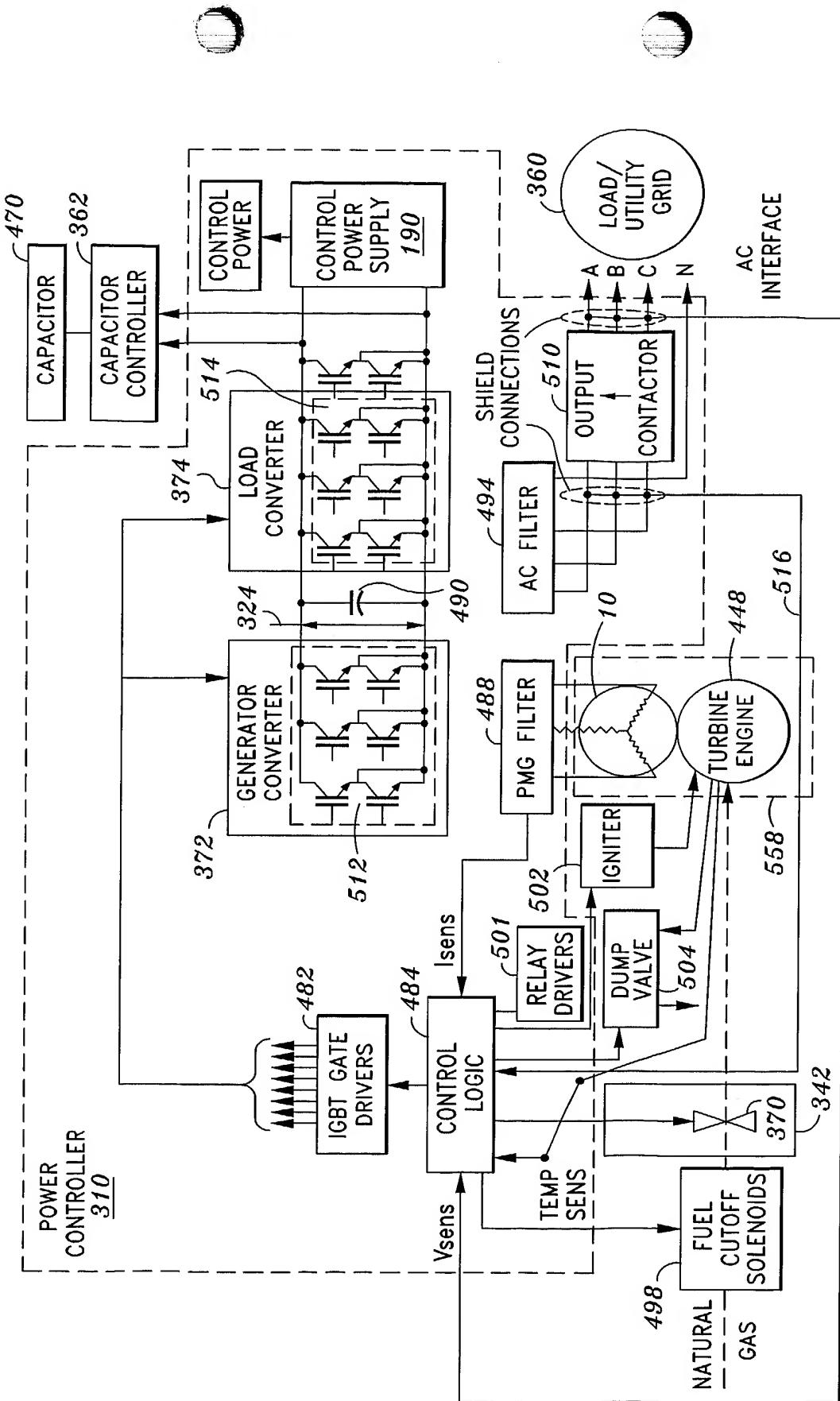
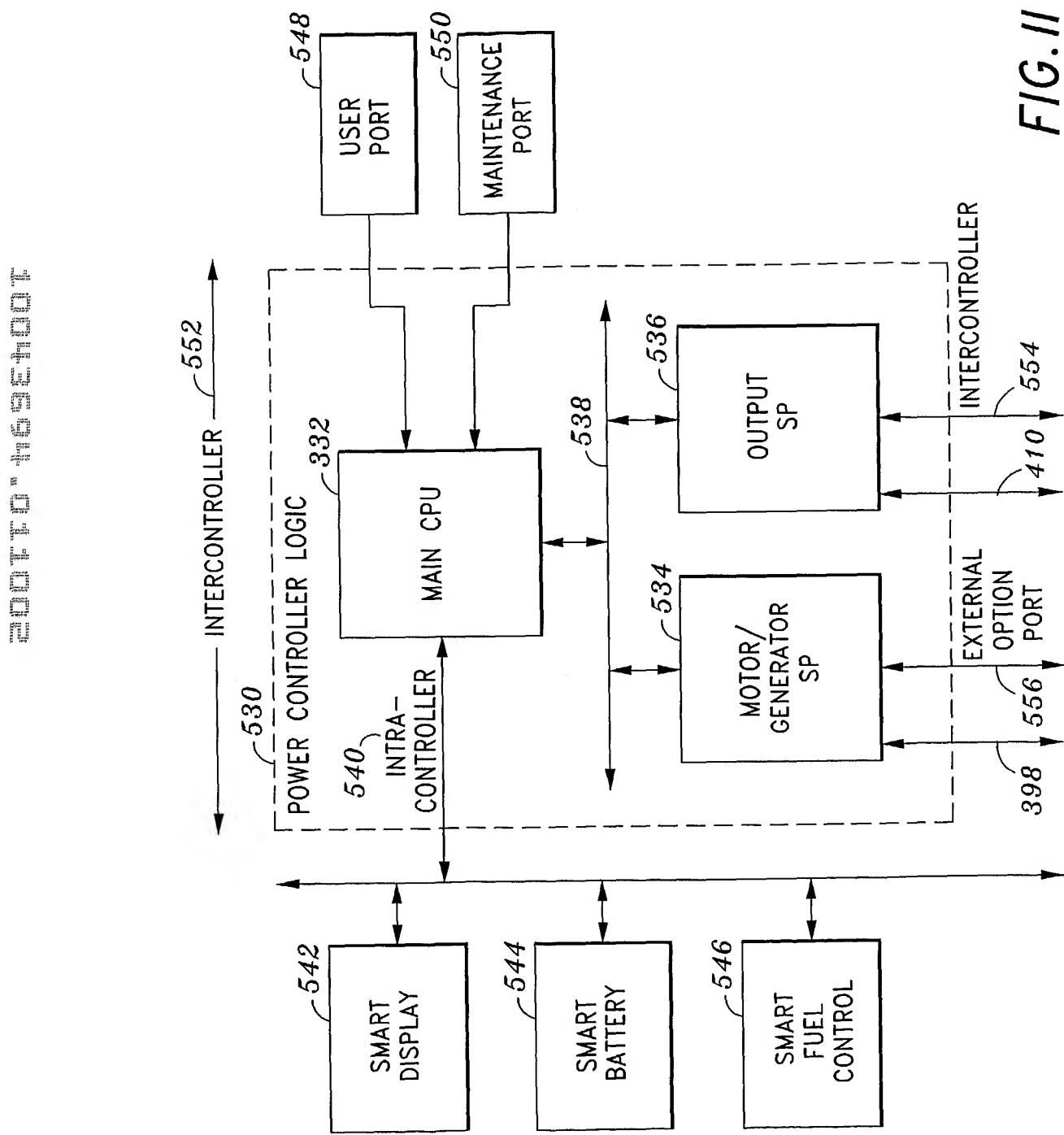


FIG. 10

FIG. II



560 562 564 566 568 570 572 574 576 578

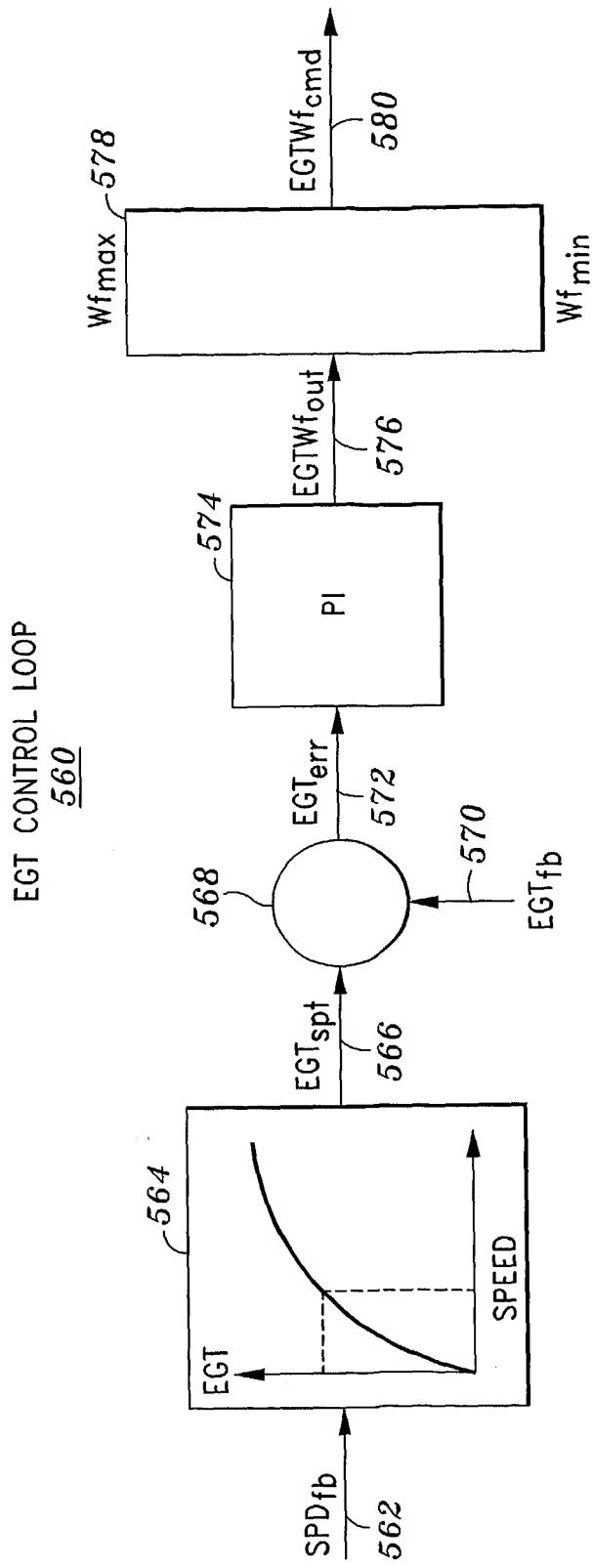


FIG. 12

SPEED CONTROL MODE LOOP

582

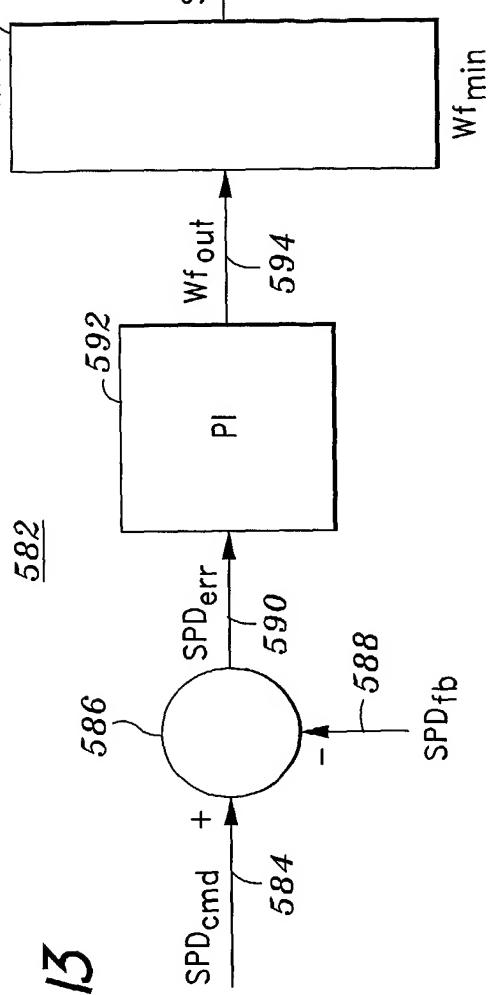


FIG. 13

POWER CONTROL LOOP

600

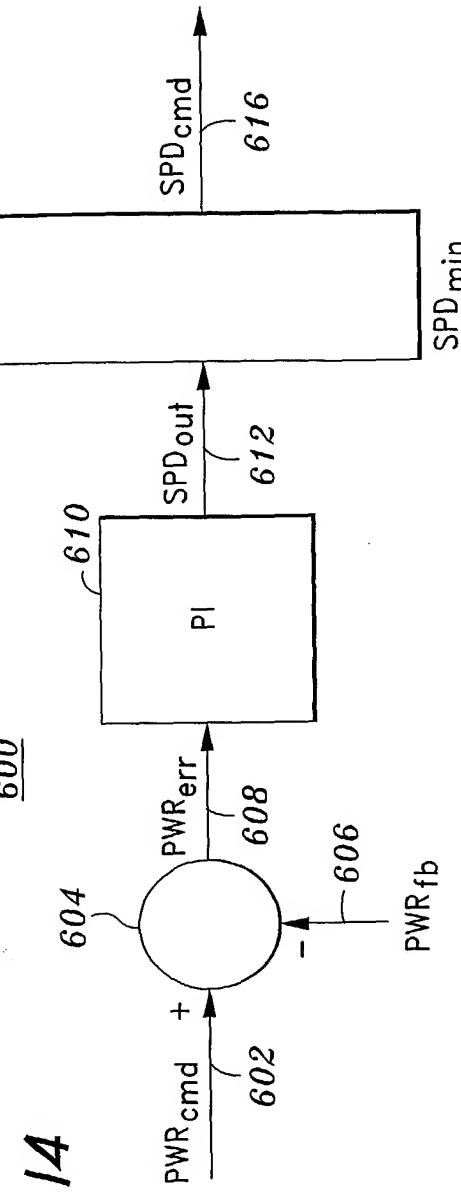


FIG. 14

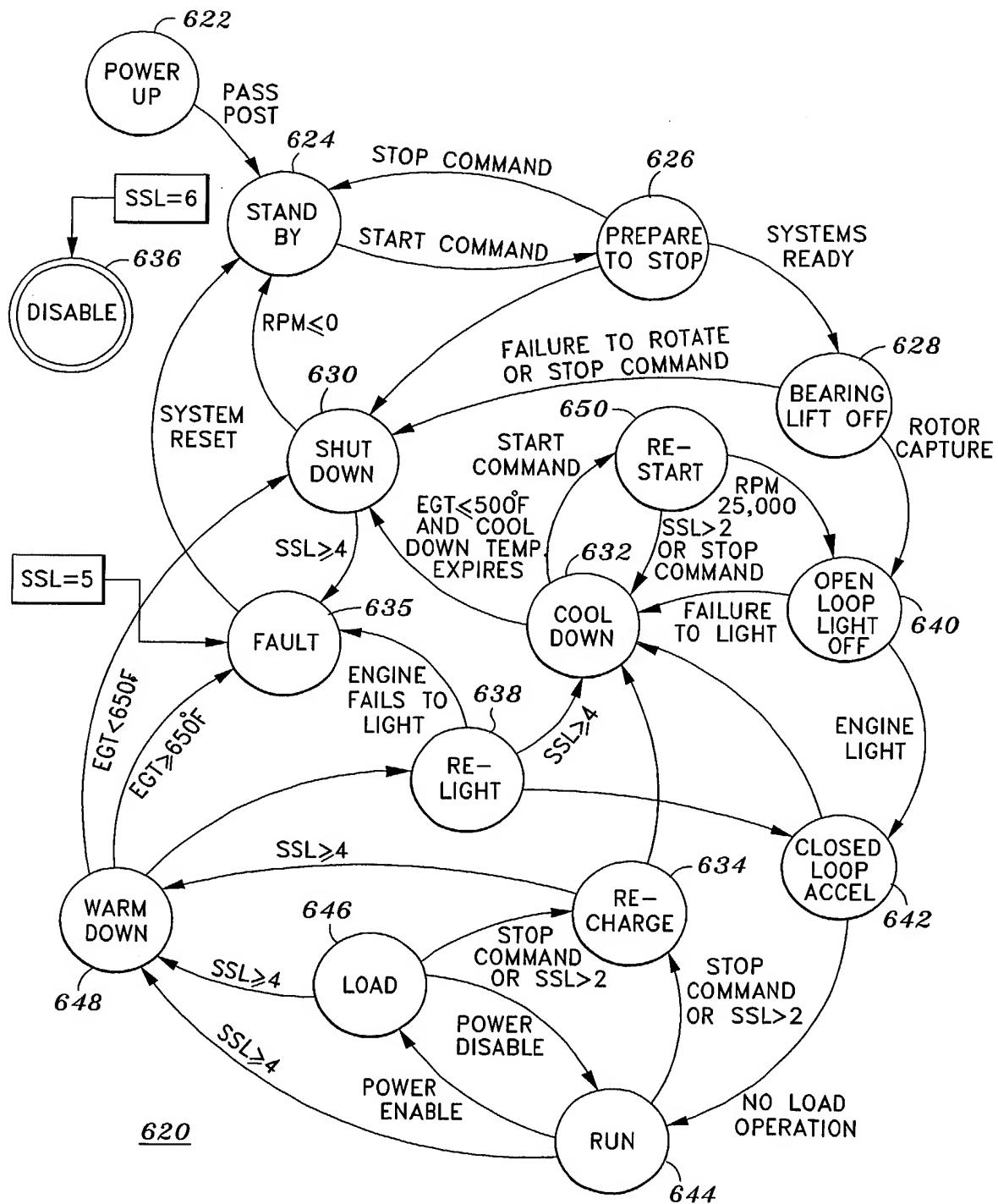


FIG. 15

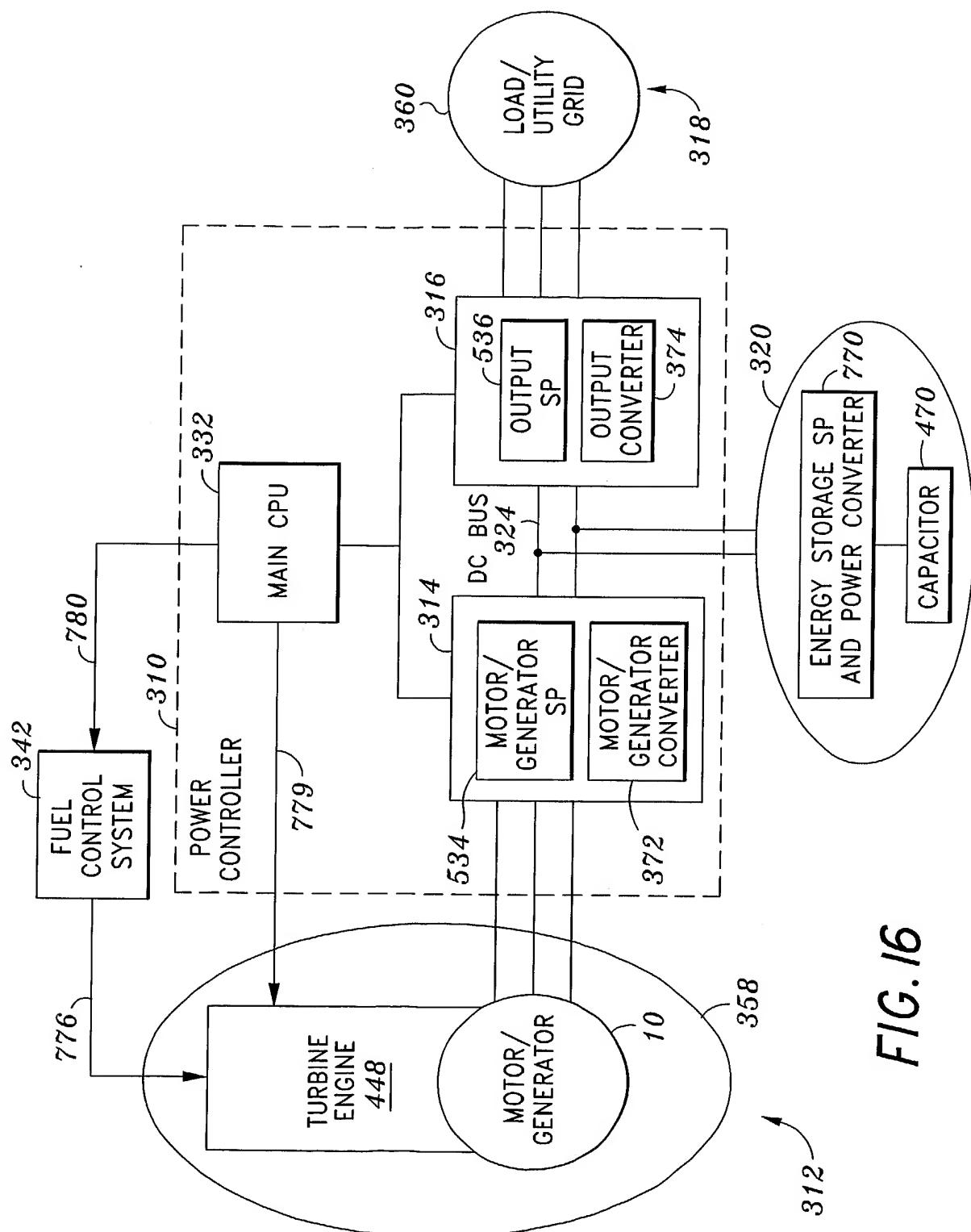


FIG. 16

FIGURE 17

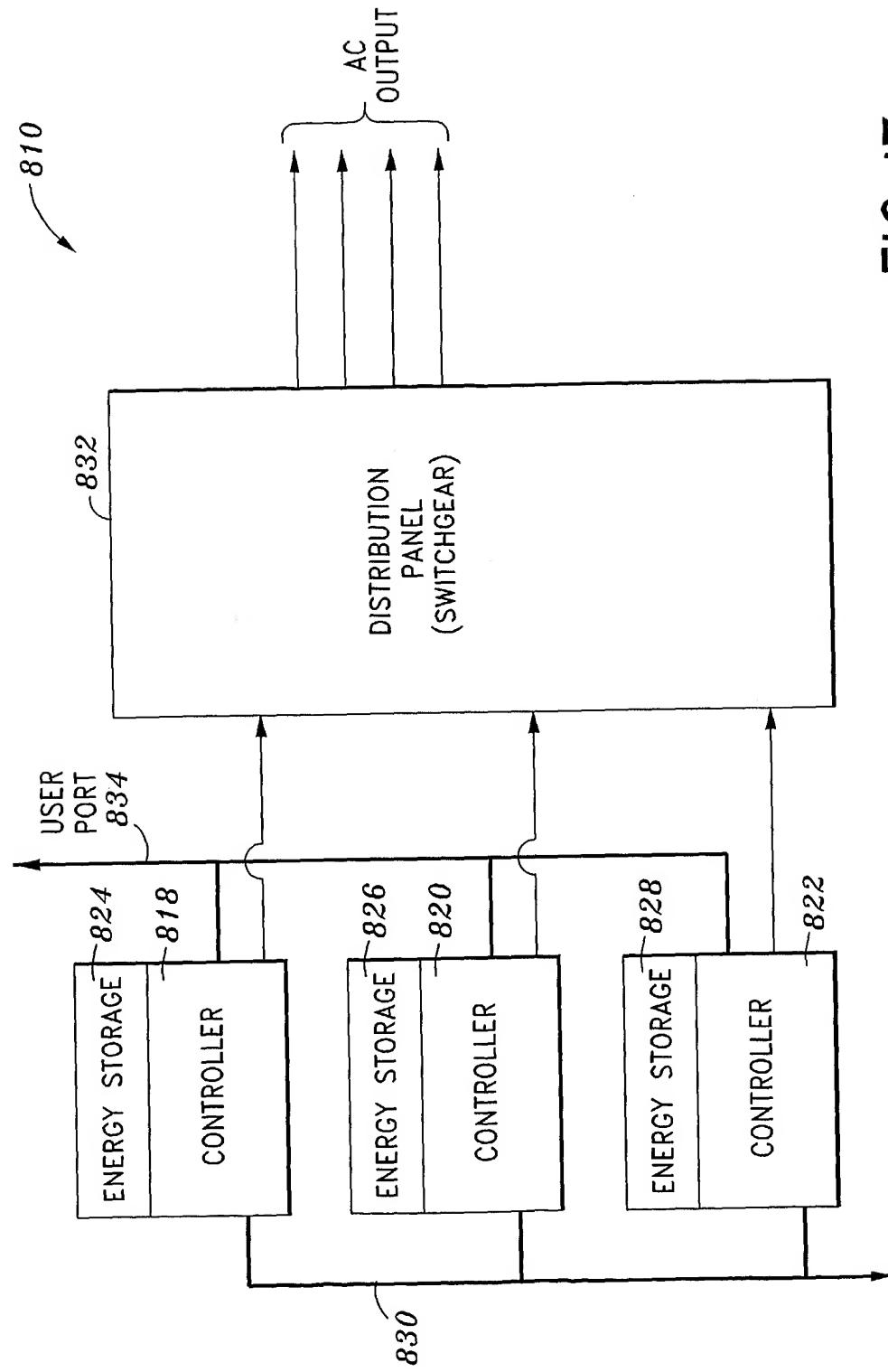


FIG. 17

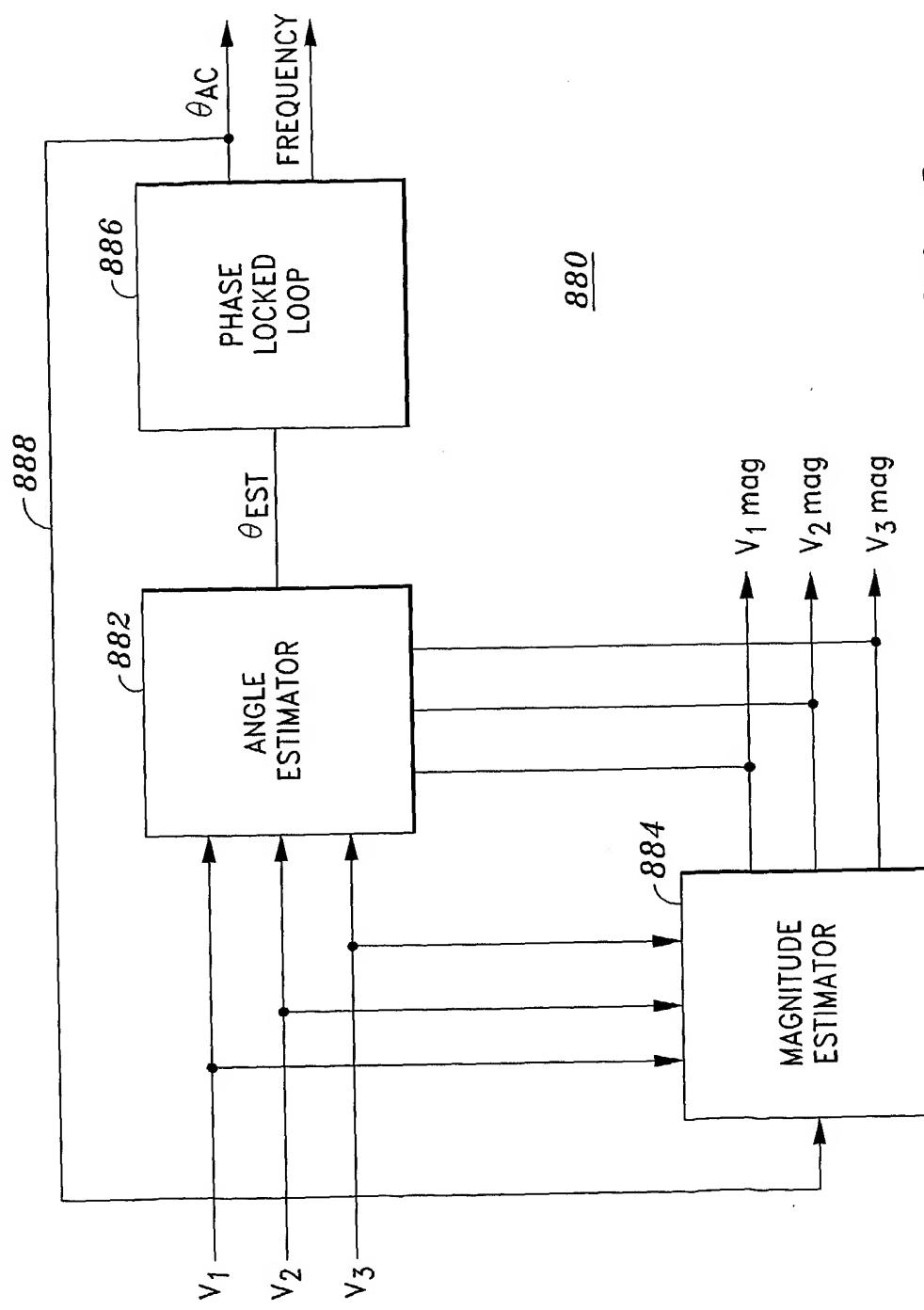


FIG. 18

FIGURE FIGURE FIGURE

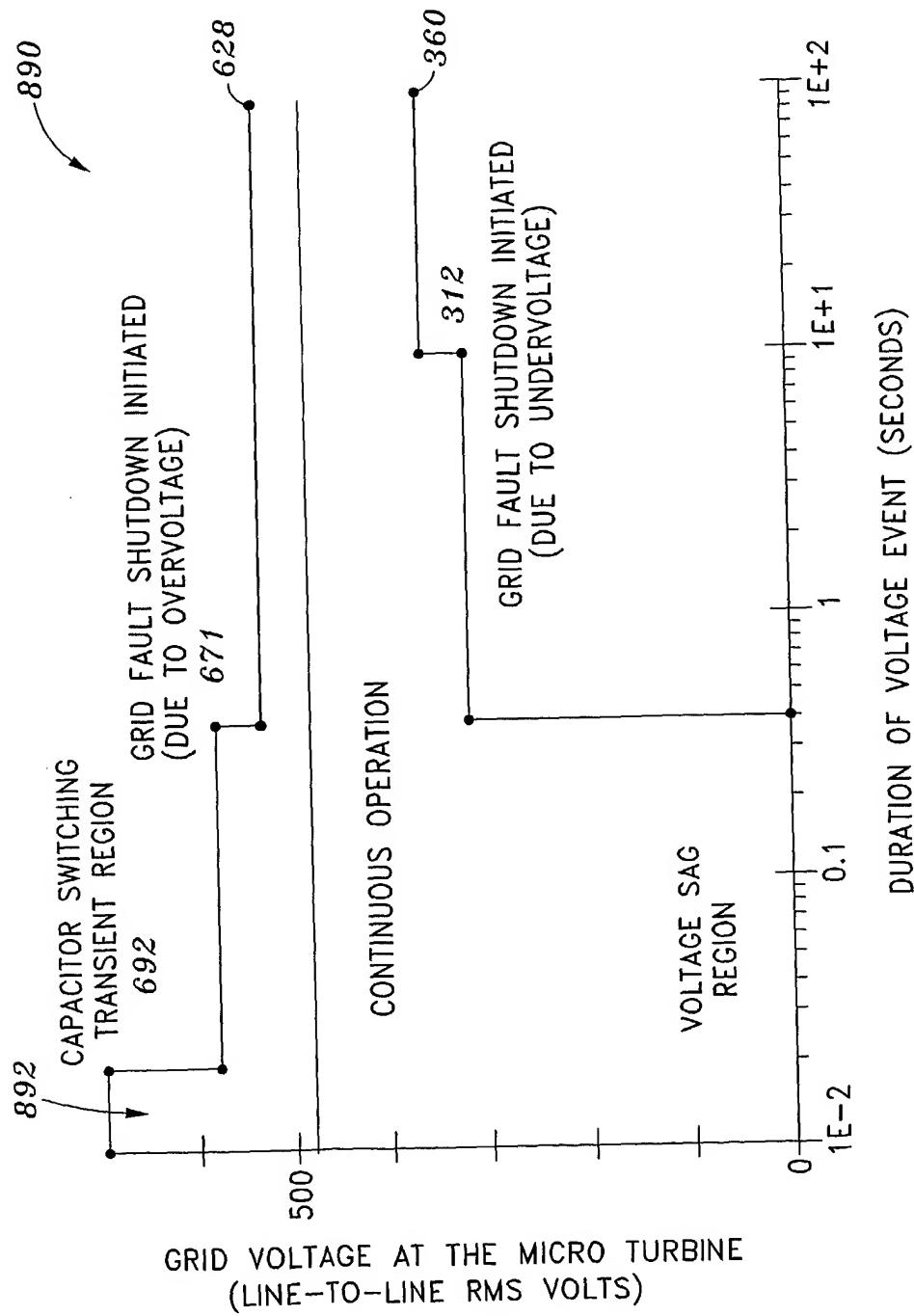


FIG. 19

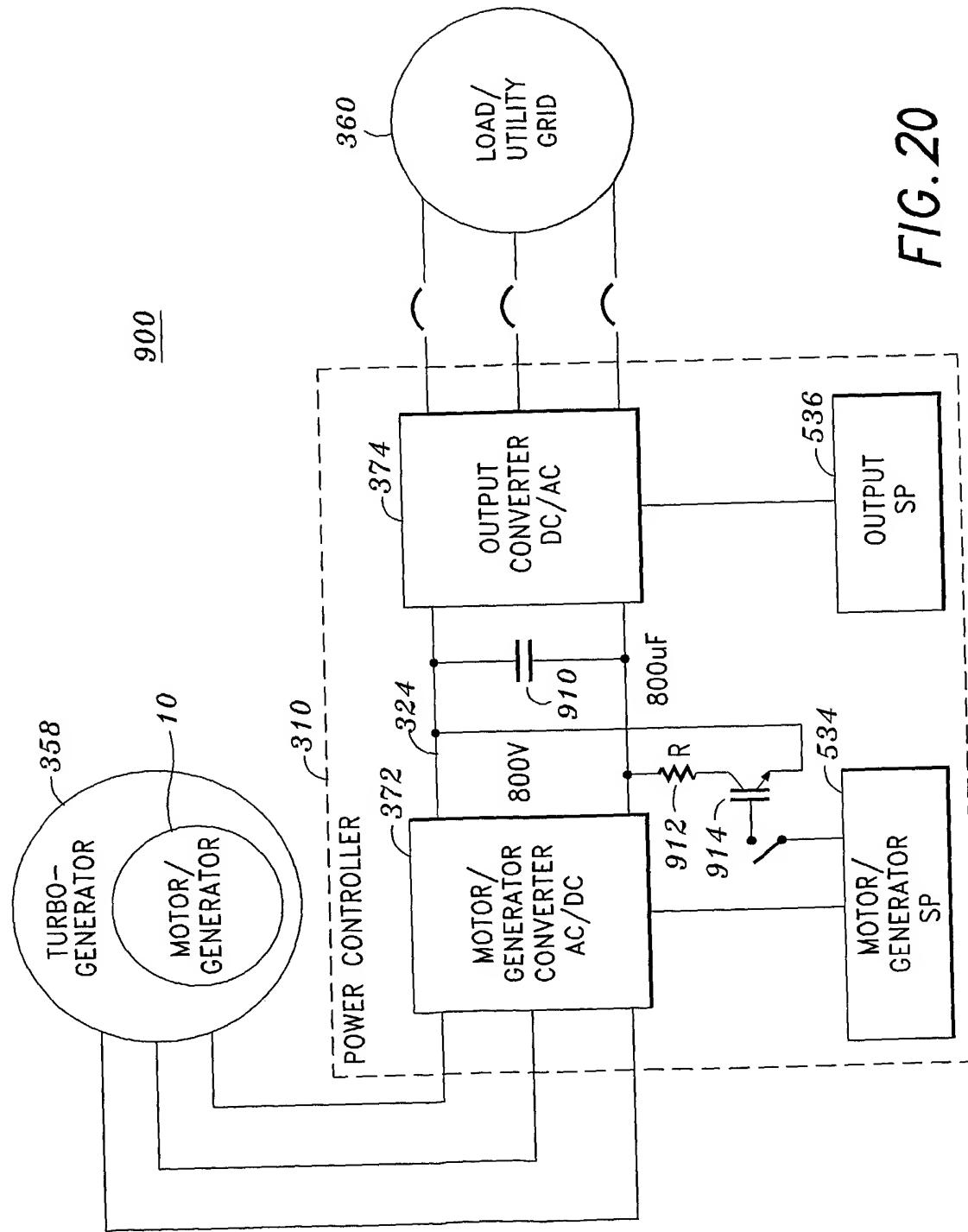


FIG. 20

FIG. 21

